Patent Attorney's Docket No.: 042390.P17989

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n Re Application of: John B. Halbert et al.

Application No. 10/750,038

Filed: 12/30/2003

For: METHOD AND APPARATUS FOR

MULTIPLE ROW CACHES PER

BANK

Examiner: Le, Thong Quoc

Art Unit:

2827

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Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph): 37 C.F.R. §1.97(b). Χ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following: A statement pursuant to 37 C.F.R. §1.97(e) or A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p). 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following: A statement pursuant to 37 C.F.R. §1.97(e); and (1) (2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement. If there are any additional charges, please charge Deposit Account No. 02-2666. Respectfully submitted, BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: August 15, 2005

Michael J. Mallie Reg. No. 36,591

12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025 (408) 720-8300

Docket No.: 042390.P17989

Complete if Known Substitute for Form 1449/PTO Application Number 10/750,038 INFORMATION DISCLOSURE Filing Date 12/30/2003 STATEMENT BY APPLICANT First Named Inventor: John B. Halbert et al. (use as many sheets as necessary) Art Unit 2827 **Examiner Name** Le, Thong Quoc 2 Attorney Docket Number 042390.P17989 Sheet 1 of **U.S. PATENT DOCUMENTS** Examiner Cite No. Publication Date Name of Patentee or Initials* **Document Number** MM-DD-YYYY Applicant of Cited Document Number-Kind Code² (If known) 4.608.666 08-26-1986 Uchida 4,817,054 US-03-28-1989 Banerjee et al. US-5,703,810 12-30-1997 Nagy US-5,748,554 05-05-1998 Barth et al.

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		FO	REIGN PATEN	T DOCUMENTS		
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		PCT/US2004/043672	04-20-2005	PCT International Search Report and Written Opinion		
		EP 0 463 617 A2	01-02-1992	Kabushiki Kaisha Toshiba		
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Bondurant et al.

Lee et al.

Barth et al.

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					Examiner Name	Le, Thong Quoc		
Sheet	2	2 of 2 Attorney Docket Number 042390.P17989						
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	Katsumi Dosaka et al., "A 100-MHz 4-Mb Cache DRAM with Fast Copy-Back Scheme", IEEE Journal of Solid-State Circuits, Vol. 27, No. 11, November 1992, XP 000320439, pages 1534-1538.							
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